Substitute for form 1449A&B/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 3

	Complete if Known		
Application Number	10/799,409		
Filing Date	March 12, 2004		
First Named Inventor	CHONG, Yan Johnson,	Brian	D.
Art Unit	2116	BLIAM	ט.
Examiner Name	STOYNOV, Stefan		
Attomey Docket Number	015114-066910US		

015114-066910US

Examiner	Cite	Document Numb	U.S. PATENT	DOCUMENTS	
Initials.	No.'	Document Number  Number Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Pessages or Relevant
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Examiner Signature	/Stefan Stoynov/	Date Considered	01/29/2007	1
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Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached.

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			Application Number	10/799,409		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	March 12, 2004 <del>CHONG, Yan</del> Johnson, Bria	
		First Named Inventor				
		Art Unit	2116			
		Examiner Name	STOYNOV, Stefan			
Sheet	2	of	3	Attorney Docket Number	015114-066910US	

Examiner	Cite	Document Number	U.S. PATENT DO	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No.1	Number Kind Code <sup>2 (# Anown)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
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	Examiner Signature	/Stefan Stoynov/	Date Considered	01/29/2007

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Substitute for form 1449A&B/PTO Complete if Known Application Number 10/799,409 INFORMATION DISCLOSURE Filing Date March 12, 2004 STATEMENT BY APPLICANT First Named Inventor CHONG, Yan Johnson, Brian D. Art Unit 2116 (Use as many sheets as necessary) Examiner Name STOYNOV, Stefan 3 Sheet 3 Attorney Docket Number 015114-066910US

	1 04		U.S. PATENT DO	CUMENTS	
Examiner Initials*	Cite No.	Document Number  Number Kind Code <sup>2 (F known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
SS	CM	US 2005/0184776	August 2005	Pilo et al.	riguies Appear
SS	CN	US 2005/0200390	September 2005	Starr et al.	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city	Т
SS	CO MICHAEL B. BENDAK et al., "CMOS VLSI Implementation of Gigabyte/Second Computer Network Links", IEEE, 1996, pp. 269-272		TE
SS	СР	"Virtex-II 1.5V Field-Programmable Gate Arrays", XILINX, DS031-2 (v1.5), April 2, 2001, pp. 1-36	
SS	CQ	ANDREA BONI et al., "LVDS I/O Interface for Gb/s-per-Pin Operation in 0.35-μm CMOS" IEEE Journal of Solid-State Circuits, vol. 36, no. 4, April 2001, pp. 706-711	
SS	CR	*APEX II Programmable Logic Device Family*, Altera Corporation, May 2001, pp. 1-8, 38-59	
SS	CS *APEX 20K Programmable Logic Device Family*, Altera Corporation, May 2001, pp. 1-8, 37-47		  -
kaminer gnature		/Stefan Stoynov/ Date Considered 01/29/2007	<u> </u>

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